
Subject: : AmigaOS4

Topic: : New Sam460cr boards will hit the road soon!

Re: New Sam460cr boards will hit the road soon!

Author: : kas1e

Date: : 2021/4/28 21:06:53

URL:

@max

Yeah, that what i have when set debug output to serial with level 1:

Vendor Specific Data Block

it8212ide.device 53.20 (24.9.2014)

[it8212ide/dev_init] Nothing started, bailing out

lsi53c8xx.device 53.20 (24.9.2014)

[lsi53c8xx/dev_init] Nothing started, bailing out

sam460sata.device 53.20 (24.9.2014)

[sam460sata/dev_init] Running on PPC460ex

[sam460sata/dev_init] not in SATA mode

[sam460sata/dev_init] Nothing started, bailing out

sii0680ide.device 53.20 (24.9.2014)

[sii0680ide/dev_init] Nothing started, bailing out

sii3112ide.device 53.20 (24.9.2014)

[sii3112ide/dev_init] Nothing started, bailing out

sii3114ide.device 53.20 (24.9.2014)

[sii3114ide/dev_init] Found chip #0

[sii3114ide/init_port] ---> Port 0

[sii3114ide/init_port] IOBase 00001000, AltBase 0000100A

[sii3114ide/init_port] bmcr_base 00001020

[sii3114ide/init_port] MMIOBase 80000000

[sii3114ide/init_port] Config not forced, scanning port 0...

[sii3114ide/reg_config] assuming device 0 is TYPE_NONE

[sii3114ide/reg_config] assuming device 1 is TYPE_NONE

[sii3114ide/reg_config] there seems to be a device 0, assuming TYPE_UNKN

[sii3114ide/reg_config] there is no device 1 (sc 00 sn 00)

[sii3114ide/reg_config] device 0 is ATA

[sii3114ide/init_port] 1 device(s) on port

[sii3114ide/port_list] 0 Master : 'ATA'

[sii3114ide/port_list] 1 Slave : 'no device'

[sii3114ide/init_port] Starting 'sii3114ide.device - chip 0 port 0' task

[sii3114ide/int_install_handler] Installing handler for irq 48

[sii3114ide/init_unit] Trying to configure unit 0

[sii3114ide/irq_wait] timed out

[sii3114ide/exec_pio_data_in_cmd] <- here

[sii3114ide/ata_read_drive_properties] Unit 0 returned error 255, failbits 00000000h, timeout 0

[sii3114ide/init_port] Port 0 task started
[sii3114ide/init_port] ---> Port 1
[sii3114ide/init_port] IOBase 00001010, AltBase 0000101A
[sii3114ide/init_port] bmcr_base 00001028
[sii3114ide/init_port] MMIOBase 80000000
[sii3114ide/init_port] Config not forced, scanning port 1...
[sii3114ide/reg_config] assuming device 0 is TYPE_NONE
[sii3114ide/reg_config] assuming device 1 is TYPE_NONE
[sii3114ide/reg_config] there is no device 0 (sc 00 sn 00)
[sii3114ide/reg_config] there is no device 1 (sc 00 sn 00)
[sii3114ide/init_port] 0 device(s) on port
[sii3114ide/dev_init] All done

sii3512ide.device 53.20 (24.9.2014)

[sii3512ide/dev_init] Nothing started, bailing out
[sii3114ide/find_unit] unitnum 4 invalid (!chip)
[sii3114ide/find_unit] unitnum 5 invalid (!chip)
[sii3114ide/find_unit] unitnum 6 invalid (!chip)
[sii3114ide/find_unit] unitnum 7 invalid (!chip)
[sii3114ide/find_unit] unitnum 8 invalid (!chip)
[sii3114ide/find_unit] unitnum 9 invalid (!chip)
[sii3114ide/find_unit] unitnum 10 invalid (!chip)
[sii3114ide/find_unit] unitnum 11 invalid (!chip)
[sii3114ide/find_unit] unitnum 12 invalid (!chip)
[sii3114ide/find_unit] unitnum 13 invalid (!chip)
[sii3114ide/find_unit] unitnum 14 invalid (!chip)
[sii3114ide/find_unit] unitnum 15 invalid (!chip)
[OHCI] Enabled USB port on the 460ex processor.
[OHCI] Forcing powering down before a reset

Seems that sixxxxx.device just can't do things properly ? I see that in version 53.21 of sixxxxx.device there was this error fixed:

Quote:

sii3114ide.device 53.21 (27.6.2017) <cmincea>

- Replaced ReadConfigLong(PCI_BASE_ADDRESS_x) calls with their PCI interface variant GetResourceRange(x)->BaseAddress to get the correct base address instead of the *bus* base address.

Can it be related anyhow ?