

Subject: : AmigaOS4

Topic: : DMA buffer for PCI busmaster headache

Re: DMA buffer for PCI busmaster headache

Author: : geennaam

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URL:

@ncafferkey

So the only difference is that I do not need to perform a Cache flush manually?

What I do now is:

1. allocate a contiguous buffer which is physical aligned and is not allowed to be relocated with AllocVecTags(). This function returns the virtual address. (for OS4 access)

2. acquire the physical address in supervisor mode (for DMA controller access)

3. Perform a CacheClearE() after I've completed a write sequency to the memory region with the virtual address.

Anyways, that works like a charm now. But if it is more OS4 legal to do it with StartDMA()/EndDMA then I will switch of course.