

---

Subject: : AmigaOS4

Topic: : X5000 No Longer Booting OS4!

Re: X5000 No Longer Booting OS4!

Author: : AcillClassics

Date: : 2020/12/26 18:34:40

URL:

Saw the DDR sticks showing as not the same, which isn't accurate so I replaced them with another set. The original set was in first, then the newer set. Same issue with loading, but they both reported the same. Tried with just one stick and no change.

U-Boot 2014.04 (Oct 17 2016 - 08:22:22)

CPU0: P5020, Version: 2.0, (0x82200020)

Core: e5500, Version: 1.2, (0x80240012)

Clock Configuration:

CPU0:1995 MHz, CPU1:1995 MHz,

CCB:798 MHz,

DDR:665 MHz (1330 MT/s data rate) (Asynchronous), LBC:49.875 MHz

FMAN1: 498.750 MHz

QMAN: 399 MHz

PME: 399 MHz

L1: D-cache 32 KiB enabled

I-cache 32 KiB enabled

Reset Configuration Word (RCW):

00000000: 0c540000 00000000 1e1e0000 00000000

00000010: 44808c00 ff002000 68000000 45000000

00000020: 00000000 00000000 00000000 0003000f

00000030: a0000000 00000000 00000000 00000000

Board: CYRUS

36-bit Addressing

I2C: ready

SPI: ready

DRAM: Initializing....using SPD

DDR: failed to read SPD from address 81

Detected UDIMM KHX1600C10D3/8G

There is no rank on CS0 for controller 0.

Not enough bank(chip-select) for CS0+CS1 on controller 0, interleaving disabled!

Not all DIMMs are identical. Memory controller interleaving disabled.

6 GiB left unmapped

8 GiB (DDR3, 64-bit, CL=9, ECC off)

DDR Chip-Select Interleaving Mode: CS0+CS1

Local Bus Controller Registers

BR0 0xE0001001 OR0 0xFFF00010  
BR1 0xE1001001 OR1 0xFFF00010  
BR2 0x00000000 OR2 0x00000000  
BR3 0x00000000 OR3 0x00000000  
BR4 0x00000000 OR4 0x00000000  
BR5 0x00000000 OR5 0x00000000  
BR6 0x00000000 OR6 0x00000000  
BR7 0x00000000 OR7 0x00000000  
LBCR 0x40000000 LCRR 0x80010004

L2: 512 KiB enabled

Corenet Platform Cache: 2 MiB enabled

SERDES: bank 2 disabled

MMC: FSL\_SDHC: 0

EEPROM: NXID v0

PCIe1: Root Complex, x4 gen2, regs @ 0xfe200000

01:00.0 - 1002:683d - Display controller

01:00.1 - 1002:aab0 - Multimedia device

PCIe1: Bus 00 - 01

PCIe2: Root Complex, x4 gen2, regs @ 0xfe201000

03:00.0 - 111d:8092 - Bridge device

04:01.0 - 111d:8092 - Bridge device

05:00.0 - 12d8:e111 - Bridge device

04:02.0 - 111d:8092 - Bridge device

07:00.0 - 10ec:8168 - Network controller

04:03.0 - 111d:8092 - Bridge device

04:08.0 - 111d:8092 - Bridge device

04:10.0 - 111d:8092 - Bridge device

PCIe2: Bus 02 - 0a

PCIe3: disabled

PCIe4: disabled

Looking for VGA

PINS: 0xfc000808

videoboot: Booting PCI video card bus 1, function 0, device 0

Begin BIOS POST

End BIOS POST

Setting VESA Mode

Found required VESA mode

About to set mode 279

About to get mode

Got mode 49431 (0xc117)

OK

XSize = 1024 YSize = 768 Base = 0xe0000000

mmio\_base = 0x80000000

bgx = 0xad55ad55 fgx = 0x0

In: serial

Out: serial

Err: serial

ID: NXID v0

SN: 116330024

UID: 4429431001910050e4d4a080a0800000

Errata: yyyyyBGy

Build date: 2015/06/11 14:42:47

CRC: 3d2fd346

SCSI: scanning bus for devices...

Found 0 device(s).

Net: Initializing Fman

MMC read: dev # 0, block # 1130, count 128 ...

Fman1: Uploading microcode version 106.1.0

Tuning PHY @ 3

Tuning PHY @ 7

FM1@DTSEC4 [PRIME], FM1@DTSEC5

MMC read: dev # 0, block # 1280, count 512 ... 512 blocks read: OK

MMC read: dev # 0, block # 65536, count 131072 ... 131072 blocks read: OK

USB0: USB EHCI 1.00

scanning bus 0 for devices... 4 USB Device(s) found

USB1: USB EHCI 1.00

scanning bus 1 for devices... 2 USB Device(s) found

SATA0 (3 Gbps)

SATA1 (1.5 Gbps)

CE at device

scanning bus for devices...

Not ready...

Not ready...

Not ready...

Not ready...

Not ready...

Not ready...

Not ready...

Not ready...

Not ready...

Not ready...

Device 0: (1:0) Vendor: HL-DT-ST Prod.: DVDROM GH24NSC0 Rev: LI00

Type: Removable CD ROM

Capacity: not available

Found 1 device(s).

## Starting application at 0x00200000 ...

Loading finished [#####] 100 %

About to call loader entry at 0x7BD02020