
Subject: : E-UAE

Topic: : e-uaes from os4depot, fresh recompile (post #10)

Re: e-uaes from os4depot, fresh recompile (post #10)

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Date: : 2018/11/23 22:43:12

URL:

@kas1e

tested on my SAM460 and works fine.

BTW, can't remember, but if I enable JIT, shouldn't it show on bottom-leds 'JIT'?

-UAE 1.0.0

Build date: Nov 22 2018 23:26:52

SDLGFX: Initialized.

SDLGFX: Using SDL version 1.2.15.

SDLGFX: Display is 32 bits deep.

SDLGFX: Found screenmode: 1280x1024.

SDLGFX: Found screenmode: 1024x768.

SDLGFX: Found screenmode: 800x600.

SDLGFX: Found screenmode: 640x480.

Found 0 joystick(s)

Found amiga raw keyboard mapping

Using raw keymap

Opening cfgfile '.uaerc'...okay.

unknown config entry: 'amiga.screen_type=public'

unknown config entry: 'amiga.publicscreen='

unknown config entry: 'amiga.use_dither=false'

EClock frequency:1155.000010 MHz

Found amiga raw keyboard mapping

Using raw keymap

Rexx port "UAE" installed.

Found amiga raw keyboard mapping

Using raw keymap

SDLGFX: Added P96 mode: 640x480x32

SDLGFX: Added P96 mode: 640x480x8

SDLGFX: Added P96 mode: 800x600x32

SDLGFX: Added P96 mode: 800x600x8

SDLGFX: Added P96 mode: 1024x768x32

SDLGFX: Added P96 mode: 1024x768x8

SDLGFX: Added P96 mode: 1280x1024x32

SDLGFX: Added P96 mode: 1280x1024x8

Enabling argb32 byte-swapping for P96.
Building CPU table for configuration: 68000 24-bit addressing
1866 CPU functions
Building CPU function table, 44736 opcodes (0 0 1).
JIT: Building compiler function table.
JIT: Allocation of translation cache...
JIT: Selected processor type has no cache, leaving
reset at 0
JIT: Compiling reset
PAL mode, 50Hz (h=227 v=312)
chipmem cleared
Illegal instruction: 4e7b at 00fc0564 -> 00fc0582
SERIAL: period=372, baud=9600, hsyncs=16 PC=fc2234
PAL mode, 56Hz (h=227 v=275)
PAL mode, 50Hz (h=227 v=312)